Publication List
1. PUBLICATIONS

1.1. Awards


1.2. Books


Wim Claes, Willy Sansen en Robert Puers, Design of Wireless Autonomous Datalogger ICs, Springer, 2005


1.3. Journal papers
Bert Serneels, Eldert Geukens, Bram De Muer and Tim Piessens, "A 1.5W, 10V Output Class D Amplifier Using a Boosted Supply from a Single 3.3V Input in standard 1.8V / 3.3V 0.18μm CMOS", ISSCC Digest of Technical Papers, Feb 2012.


1.4. International conference papers


**Bart De Vuyst**, Rombouts P., "5-MHz 11-bit delay-based self-oscillating Sigma Delta modulator in 0.025mm²", IEEE Custom Integrated Circuits Conference (CICC), pp. 1-4, 2010

### 1.5. Papers published in workshop proceedings

**Tim Piessens**, "Low power RF power harvesting enabling more active tag functionality", 20th Workshop on Analog Advances in Circuit Design, AACC 2011, April 2011


**Bram De Muer** and M. Steyaert, "Fully Integrated CMOS Frequency Synthesizers for Wireless Communications", Proc. of the workshop Advances in Analog Circuit Design (AADC00), Tegernsee, April 2000.


**Yves Geerts**, Michiel Steyaert and Willy Sansen, "Circuit Design Aspects of Multi-Bit Delta-Sigma Converters",...
1.6. Articles published in books


